SHEET INDEX

CONTENTS	SHEET NO.	ISSUE NO. 1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25
SHEET INDEX SUPPORTING INFORMATION	A1	1	2	3																						
FS 1 SCAN/DISTRIBUTE CKT	B1	1	2	2																						
APP FIG. 1	C1	1	1	1																						

CONTENTS	SHEET NO.	ISSUE NO. 1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25
CIRCUIT NOTES EQUIPMENT NOTES INFORMATION NOTES CROSS CONNECTION NOTES	D1	1	1	1																						
CAD 1	G1	1	1	1																						
CAD 2,3	G2	1	1	1																						
CAD 4,5,6,7	G3	1	2	3																						
CAD 8	G4	1	2	2																						

DWG ISSUE	CD ISSUE	DATE ISSUED	DRAWN	APPD
1	1	1-18-76	CDI GDL	JWD BK TAT
2A	1 APP 1A	6-22-77	CDI GAS	JWD RSP RCA
3B	1 APP 1B	6-22-77	CDI GAS	JWD RSP JWD

SHEET INDEX NOTES

1. WHEN CHANGES ARE MADE IN THIS DRAWING, ONLY THOSE SHEETS AFFECTED WILL BE REISSUED.
2. THIS SHEET INDEX WILL BE REISSUED AND BROUGHT UP TO DATE EACH TIME ANY SHEET OF THE DRAWING IS REISSUED, OR A NEW SHEET IS ADDED.
3. THE ISSUE NUMBER ASSIGNED TO A CHANGED OR NEW SHEET WILL BE THE SAME ISSUE NUMBER AS THAT OF THE SHEET INDEX.
4. SHEETS THAT ARE NOT CHANGED WILL RETAIN THEIR EXISTING ISSUE NUMBER.
5. THE LAST ISSUE NUMBER OF THE SHEET INDEX IS RECOGNIZED AS THE LATEST ISSUE NUMBER OF THE DRAWING AS A WHOLE.

SUPPORTING INFORMATION

CATEGORY	NO.
EQUIPMENT DRAWING	J-99366J-( )
EQUIPMENT DESIGN	J-99366 (801-100-151)

NOTICE- NOT FOR USE OR DISCLOSURE OUTSIDE THE BELL SYSTEM EXCEPT UNDER WRITTEN AGREEMENT.

ISSUE 3B

IN99

COMMON SYSTEMS

COMBINED SCANNER AND SIGNAL DISTRIBUTOR MATRIX CIRCUIT

(2)

AT&TCO STANDARD

SD-94851-01-A1

8 SHEETS

BELL TELEPHONE LABORATORIES INCORPORATED

DWG SIZE 6S

PRINTED IN U. S. A.

MAY 19 1978

E-7986 (11-71)

FS I

SCAN/DISTRIBUTE CIRCUIT

BUS TERMINATOR CKT PART OF SD-94837-01 (SEE NOTE 1)

FROM BUS CONNECTOR IN

D1 (SEE NOTE 201)

GRD, DO-D15, SSYN, A1-A8, C1, MSYN, AB

330 Ω, +5, 820 Ω

FROM CIRCUITS REQUIRING SCAN POINT

S1500 S1400 S1300 S1200 S1100 S1000 S0900 S0800 S0700 S0600 S0500 S0400 S0300 S0200 S0100 S0000

S1501-S1508 S1401-S1408 S1301-S1308 S1201-S1208 S1101-S1108 S1001-S1008 S0901-S0908 S0801-S0808 S0701-S0708 S0601-S0608 S0501-S0508 S0401-S0408 S0301-S0308 S0201-S0208 S0101-S0108 S0001-S0008

S1509 S1409 S1309 S1209 S1109 S1009 S0909 S0809 S0709 S0609 S0509 S0409 S0309 S0209 S0109 S0009

D2 (SEE NOTE 203)

SCAN MATRIX

SM00
17
CP A1068

SM01-SM08
18-25
CP A1068

SM09
26
CP A1068

SC15 SC14 SC13 SC12 SC11 SC10 SC9 SC8 SC7 SC6 SC5 SC4 SC3 SC2 SC1 SC0

A1-A4, AD1-AD4 6,1,7,0, DBO, DBI, ENO, ENI, STRB, DO 9, DI-D8 9, D9 9, NC 10, NC 8, NC 5, NC 4

D2 (SEE NOTE 202)

(SEE NOTE 401)

SCAN/DISTRIBUTE CONTROLLER SCO
16
CP A1075

MSYN 6, C1 5, A1-A8 7,8,4,9,11,16,19,17, AD1-AD8 10,15,20,18, N5-N8, SSYN 27

A1-A4, EN 26, STRB 25, CLK0-CLK3 0,1,2,3

AD1,AD2,AD3,AD4 23,22,24,21

D1 (SEE NOTE 201)

DISTRIBUTE MATRIX

DM0
28
CP A1074

DM1
30
CP A1074

DM2
32
CP A1074

CLK0-CLK3 6,7,8,9, ODO 0, ODI 2, ENO 1, IDO 5, EN1 4, ID1 3

D1, DO, D3, D2, D5, D4

DA00 DB00 DA01 DB01 DA10 DB10 DA11 DB11 DA20 DB20 DA21 DB21 DA30 DB30 DA31 DB31

20 19 21 18 23 16 22 17 24 15 25 14 27 12 26 13

D2 (SEE NOTE 203)

D000A D000B D001A D001B D100A D100B D101A D101B D200A D200B D201A D201B D300A D300B D301A D301B

D002A D002B D003A D003B D102A D102B D103A D103B D202A D202B D203A D203B D302A D302B D303A D303B

D004A D004B D005A D005B D104A D104B D105A D105B D204A D204B D205A D205B D304A D304B D305A D305B

TO CIRCUITS REQUIRING DISTRIBUTE POINT

POWER AND FUSING DATA TABLE

CP	EQPT LOC	GRD TERM	+5 AB TERM.	+5 AB FUSE	-48AA TERM.	-48AA FUSE
A1075	16	13,14	12			
A1068	17	2	3		19	
A1068	18	2	3		19	
A1068	19	2	3		19	
A1068	20	2	3		19	
A1068	21	2	3		19	
A1068	22	2	3		19	
A1068	23	2	3		19	
A1068	24	2	3		19	
A1068	25	2	3		19	
A1068	26	2	3		19	
A1074	28	10	11			
A1074	30	10	11			
A1074	32	10	11			

D1 WIRING (SEE NOTE 201)

D1 WIRING TO GRD STRAP (SEE NOTE 201)

NOTE:

1. IF THE COMBINED SCANNER AND SIGNAL DISTRIBUTOR IS THE LAST UNIT, THE BUS TERMINATOR IS INSTALLED ON THE (OUT) CONNECTOR.

ISSUE 2A

COMBINED SCANNER AND SIGNAL DISTRIBUTOR MATRIX

SD-94851-01-B1

BELL TELEPHONE LABORATORIES INCORPORATED

DWG SIZE 6S

PRINTED IN U. S. A.

E-7421 (10-71)

APP FIG. I

CIRCUIT PACK

EQPT LOC	16			17			18			19			20			21			22			23			24			25			EQPT LOC
DESIG	SC0			SM00			SM01			SM02			SM03			SM04			SM05			SM06			SM07			SM08			DESIG
CODE	A1075			A1068			A1068			A1068			A1068			A1068			A1068			A1068			A1068			A1068			CODE
OPTION																															OPTION
	DESIG	TERM	LOC	DESIG	TERM	LOC	DESIG	TERM	LOC	DESIG	TERM	LOC	DESIG	TERM	LOC	DESIG	TERM	LOC	DESIG	TERM	LOC	DESIG	TERM	LOC	DESIG	TERM	LOC	DESIG	TERM	LOC	
		26	IC1		9	IC3		9	IC5		9	IC5		9	IC5		9	IC5		9	IC5		9	IC5		9	IC5		9	IC5	

CIRCUIT PACK

EQPT LOC	26						28						30						32												EQPT LOC
DESIG	SM09						DM0						DM1						DM2												DESIG
CODE	A1068						A1074						A1074						A1074												CODE
OPTION																															OPTION
	DESIG	TERM	LOC	DESIG	TERM	LOC	DESIG	TERM	LOC	DESIG	TERM	LOC	DESIG	TERM	LOC	DESIG	TERM	LOC	DESIG	TERM	LOC	DESIG	TERM	LOC	DESIG	TERM	LOC	DESIG	TERM	LOC	
		9	IC8					27	IF4					27	IF6					27	IF8										

ISSUE
1

COMBINED SCANNER AND SIGNAL DISTRIBUTOR MATRIX	SD-94851-01-C1
BELL TELEPHONE LABORATORIES INCORPORATED	DWG SIZE 6S
	PRINTED IN U.S.A.

CIRCUIT NOTES:

101.

DESIG	FUSE AMP	POTENTIAL	ONE PER	CURRENT DRAIN
AA	1-1/3	-48	APP FIG.1	320ma
AB	5A MAX	+5	APP FIG.1	775ma

BATTERY SYMBOL	VOLTAGE RANGE
+5	4.5 - 5.5*

* AT UNIT TERM. STRIP

102.

FEATURE OR OPTION	PROVIDE		
	APP FIG.	APP OR WRG	QUANTITY
COMBINED SCANNER AND SIGNAL DISTRIBUTOR MATRIX CIRCUIT	1		1 PER CKT

103.

NETWORK VALUES		
NETWORK NO.	RESISTANCE IN OHMS	CAPACITANCE IN uf

104.

RECORD OF APP FIGURES, WIRING AND APPARATUS CHANGES						
CHANGED ON ISS	IF JOB RECORDS DO NOT SPECIFY	THIS OPTION WAS FURN	SEE NOTE	USE IN CIRCUIT		
				STD	A&M	MD

EQUIPMENT NOTES:

201. DIRECT WIRING TYPE D1.
202. SPECIFIED WIRING TYPE D2 PATH B AS PER BSP-800-612-150.
203. SPECIFIED WIRING TYPE D2 PATH C,D, AND E AS PER BSP-800-612-150 26 GA WIRE.

CROSS CONNECTION NOTES:

401. CONNECT LEADS N5-N8 TO SPECIFY CONTROLLER ADDRESS. CONNECTING AN N- LEAD TO GROUND SPECIFIES A ZERO FOR THE ASSOCIATED A-BIT. LEAVING AN N- BIT OPEN SPECIFIES A LOGIC ONE FOR THE ASSOCIATED A- BIT.

INFORMATION NOTES:

301. UNLESS OTHERWISE SPECIFIED: RESISTANCE VALUES ARE IN OHMS, CAPACITANCE VALUES ARE IN MICROFARADS, VALUES PRECEDED BY THE SYMBOL +(PLUS) OR -(MINUS) ARE IN VOLTS.

ISSUE 1

COMBINED SCANNER AND SIGNAL DISTRIBUTOR MATRIX

SD-94851-01-D1

BELL TELEPHONE LABORATORIES INCORPORATED

DWG SIZE 6S

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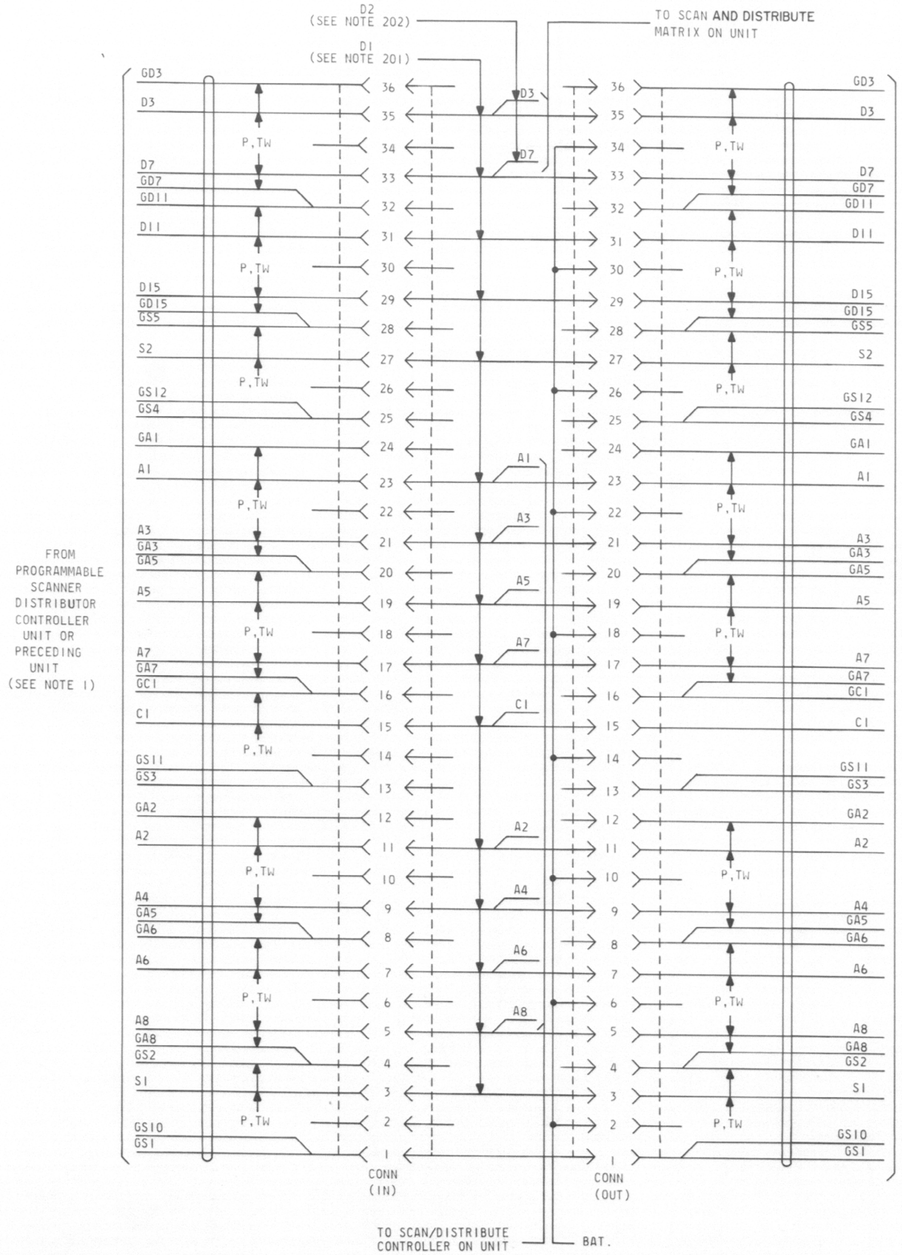
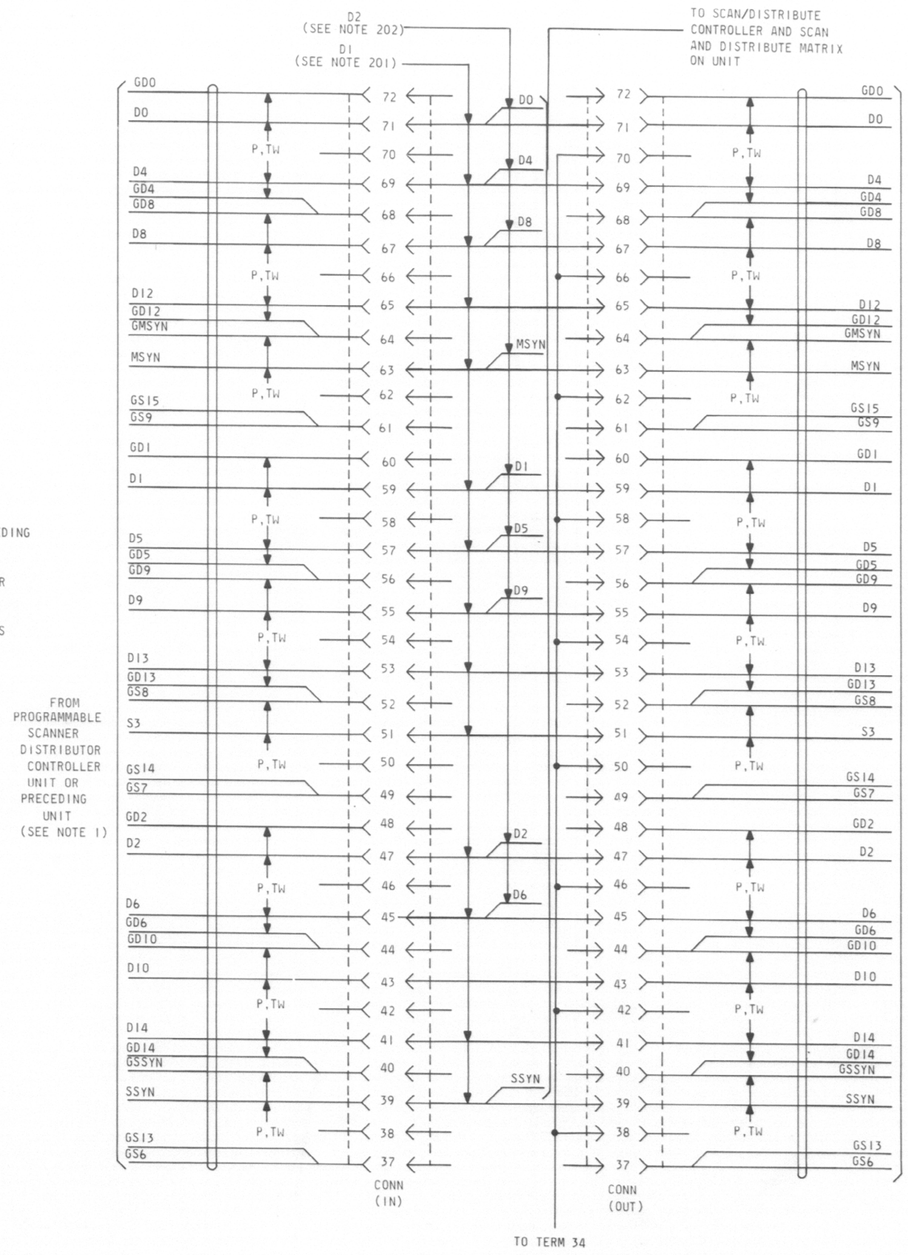
E-7421 (10-71)

CAD I





NOTES:

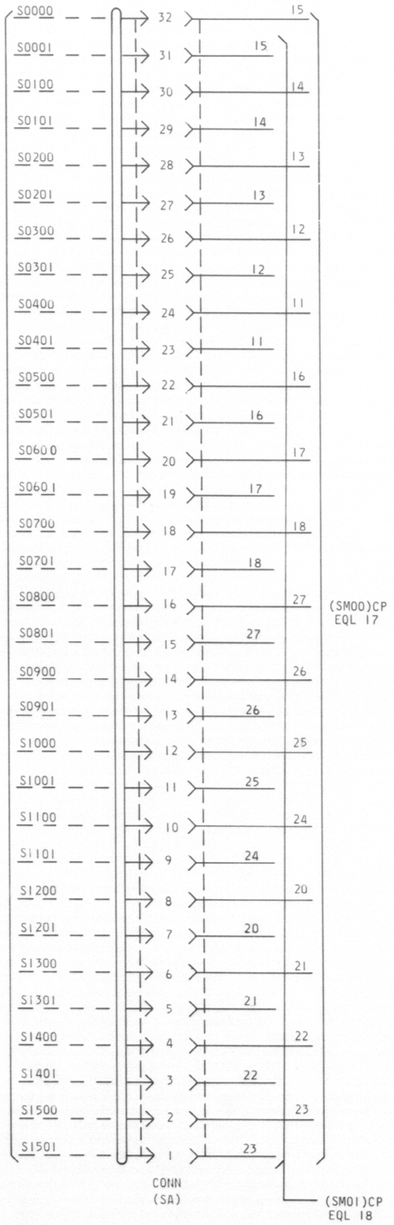
1. COMPATIBLE WITH SD-94837-01,SD-94838-01, SD-94839-01, AND SD-94851-01.
2. BUS TERMINATOR CIRCUIT SHOWN AS PART OF SD-94837-01.

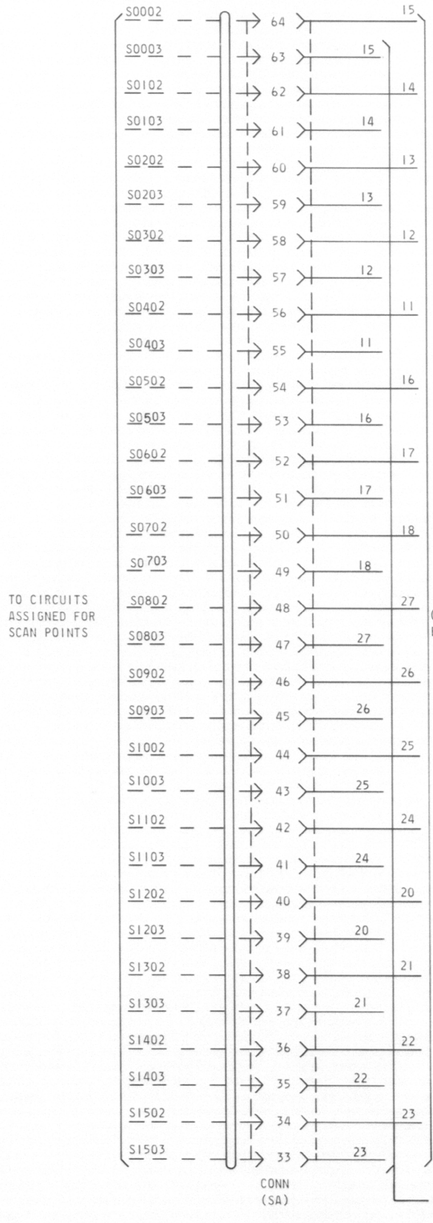
ISSUE
1

COMBINED SCANNER AND SIGNAL DISTRIBUTOR MATRIX	SD-94851-01-GI
BELL TELEPHONE LABORATORIES INCORPORATED	DWG SIZE 6S — PRINTED IN U.S.A.

E-7421 (10-71)

CAD 2





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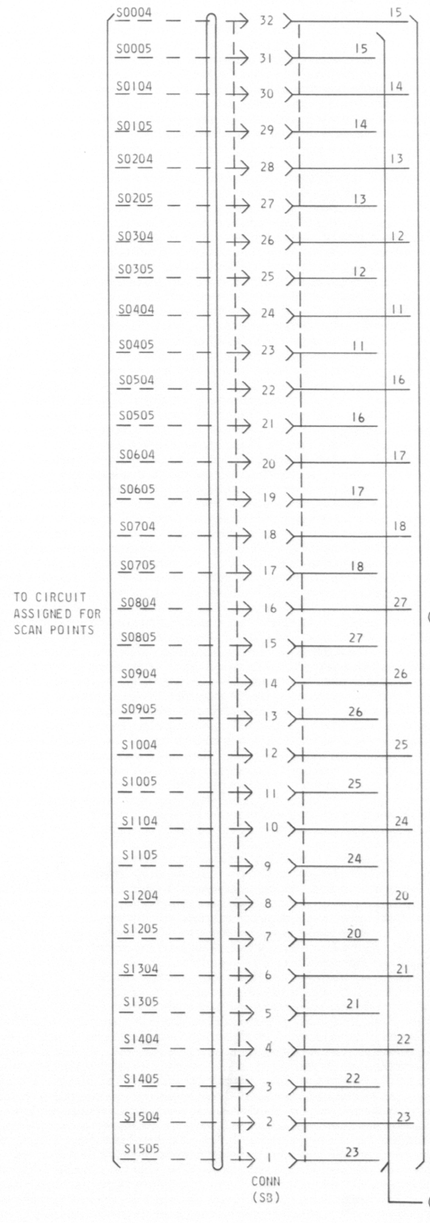

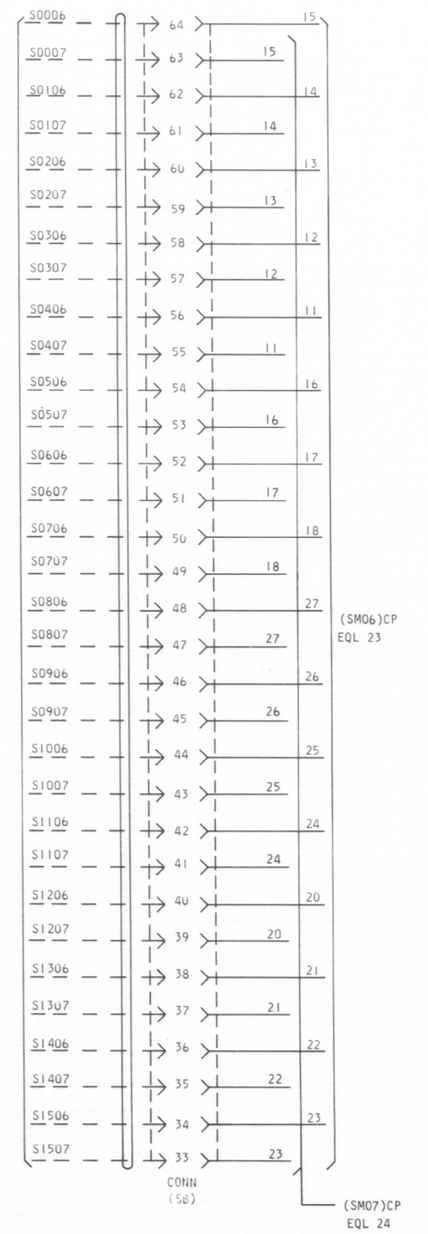

ISSUE 1

COMBINED SCANNER AND SIGNAL DISTRIBUTOR MATRIX	SD-94851-01-G2
BELL TELEPHONE LABORATORIES INCORPORATED	DWG SIZE 6S
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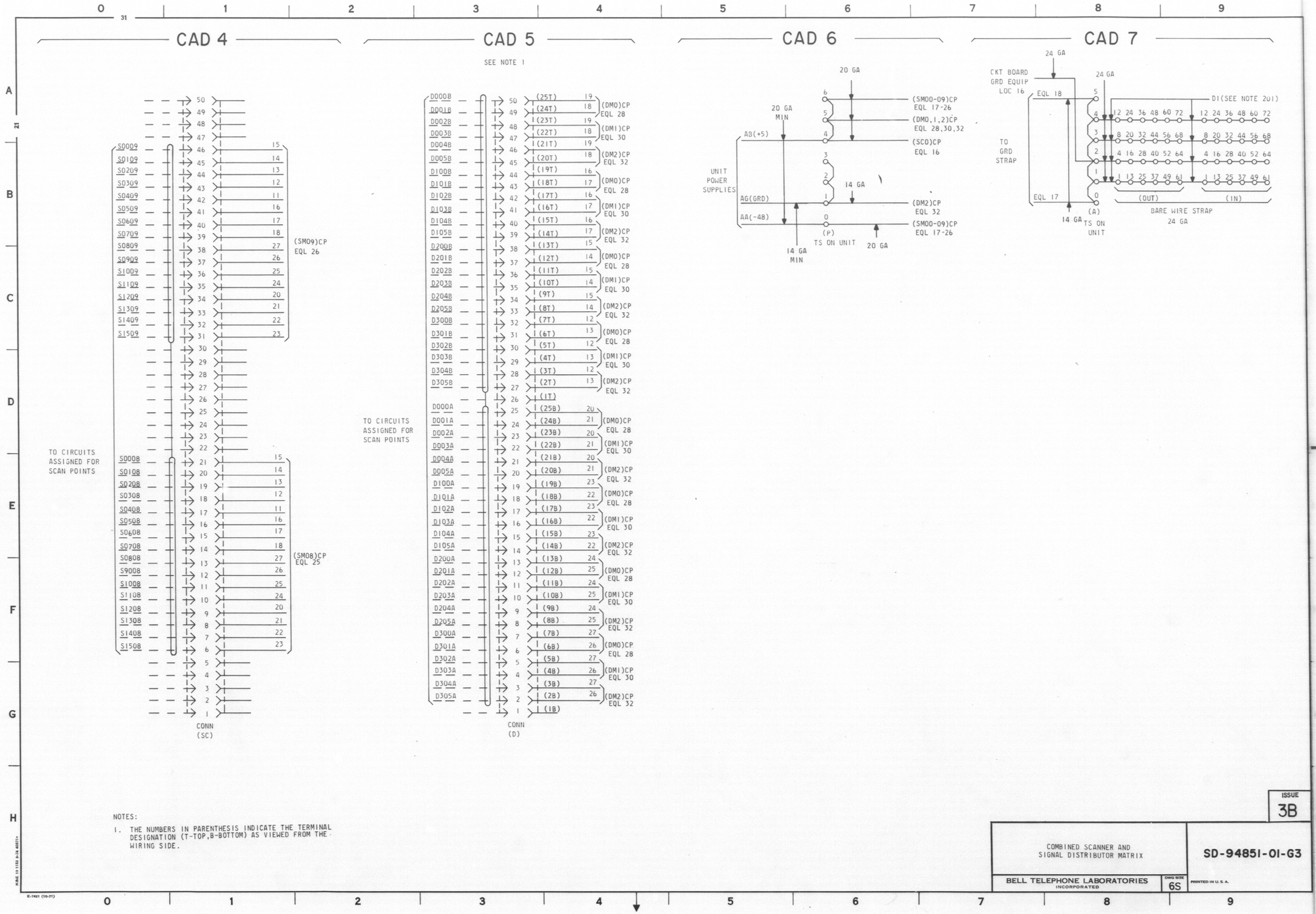
E-7421 (10-71)



NOTES:

1. THE NUMBERS IN PARENTHESIS INDICATE THE TERMINAL DESIGNATION (T-TOP, B-BOTTOM) AS VIEWED FROM THE WIRING SIDE.

ISSUE	
3B	

COMBINED SCANNER AND SIGNAL DISTRIBUTOR MATRIX

SD-94851-01-G3

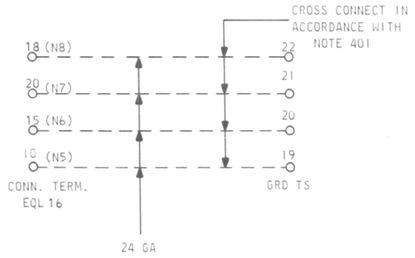
BELL TELEPHONE LABORATORIES INCORPORATED

DWG SIZE 6S

PRINTED IN U. S. A.

E-7421 (10-71)

CAD 8



ISSUE	2A
COMBINED SCANNER AND SIGNAL DISTRIBUTOR MATRIX	SD-94851-01-64
BELL TELEPHONE LABORATORIES INCORPORATED	DWG SIZE 6S
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